



1FW/B

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Anatoliy V. Tsyrganovich

Assignee: ZiLOG, Inc.

Title: "ADC With Reduced Quantization Noise and Programmable Bit Resolution"

Appl. No.: 10/821,517

Filing Date: April 9, 2004

Examiner: Lam T. Mai

Art Unit: 2819

Docket No.: ZIL-537-1P

June 2, 2006

Mail Stop Issue Fee  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
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**RULE §1.312 AMENDMENT AFTER ALLOWANCE**

Dear Sir:

Following the issuance of a Supplemental Notice of Allowability on April 19, 2006, Applicant requests the Examiner to amend the above-identified application as follows.

There are no amendments to the specification in this Amendment.

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this Amendment.

There are no amendments to the drawings in this Amendment.

The **Remarks** begin on page 7 of this Amendment.

*It's OK to enter!  
Lamman  
A-42819*